

## Electric modulation of conduction in MAPbBr<sub>3</sub> single crystals

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### ■ ABSTRACT

The resistive switching (RS) mechanism of hybrid organic-inorganic perovskites is an open question until now. Here, a switchable diode-like RS behavior in MAPbBr<sub>3</sub> single crystals using Au (or Pt) symmetry electrodes is reported. Both the high resistance state (HRS) and low resistance state (LRS) are electrode-area dependent and light responsive. We propose an electric-field-driven inner p-n junction accompanied by an interface trap-controlled SCLC mechanism to explain this switchable diode-like RS behavior in MAPbBr<sub>3</sub> single crystals.

**Keywords:** resistive switching, rectifying behavior, hybrid organic-inorganic perovskites, p-n structure

## 1. Introduction

Currently, hybrid organic-inorganic perovskites (HOIPs) have been dynamically investigated for the potential applications in solar cells [1], light-emitting diodes [2], photodetectors [3], lasers [4] and transistors [5]. For perovskite solar cells, a large electrical hysteresis in current-voltage ( $I$ - $V$ ) curves has been frequently reported, which is generally attributed to ferroelectricity [6], charge trapping [7], or ion migration [8]. Although the large hysteresis needs to be eliminated in optoelectric devices, this phenomenon inspired the observation of resistive switching (RS) behavior in hybrid perovskite films [9-16], which is exploited for multilevel data storage.

RS phenomena have attracted a very large interest in the last years. Generally, the RS devices with a capacitor-like structure could be switched by an external bias to two different resistance states, a high resistance state (HRS) and a low resistance state (LRS). Yoo *et al.* [9] reported the RS behavior for the first time in  $\text{MAPbI}_{3-x}\text{Cl}_x$  (MA is shorted for  $\text{CH}_3\text{NH}_3^+$ ) perovskite films, and proposed a mechanism based on the trap-controlled space-charge-limited conduction (SCLC). Recently, interface-type RS was observed in  $\text{Au}/\text{MAPbBr}_3/\text{ITO}$  by Guan *et al.* [10] They proposed that the switching mechanism could be due to the migration of MA vacancies and the modification of the interfacial Schottky barrier. The filamentary-type RS has also been observed in several other works on  $\text{MAPbI}_3$ -based films, where the conducting filaments were attributed to the migration of anions defects [11-13] or Ag ions from the redox reactions of Ag electrode [14,15]. Moreover, the RS behavior of HOIPs could be strongly influenced by illumination. Chai *et al.* [16] found that the set voltage could be greatly lowered under light illumination in  $\text{MAPbI}_3$ -based RS devices. However, in a previous work [17], we found no meaningful RS behavior in an individual  $\text{MAPbI}_3$  nanowire unless under light

illumination. These results clearly indicate that there remain many open questions regarding RS phenomena and switching mechanism in HOIP-based RS devices.

In this work, MAPbBr<sub>3</sub> single crystal was chosen for the conduction study, because MAPbBr<sub>3</sub> is non-polar (centrosymmetric) [18] and therefore ferroelectricity could be excluded, while the influence of grain boundaries can also be ignored. Compared with polycrystalline thin films [19,20], MAPbX<sub>3</sub> single crystals present lower trap density and much better environmental stability, which can be considered as an ideal platform for investigating their intrinsic physical properties. The electric-field-driven resistance switching accompanied by a diode-like behavior was produced in the Au/MAPbBr<sub>3</sub>/Au structure. The diode-like switchable effect can be explained by the formation of reversible p-n junctions induced by ion immigration in the crystals. The area-proportional conduction indicates an interface-type RS behavior, which is related to the charge trapping/detrapping process at the interface.

## 2. Experimental Section

The MAPbBr<sub>3</sub> single crystals were prepared from solution by inverse temperature crystallization (ITC) method [18]. The raw materials methylammonium bromide (CH<sub>3</sub>NH<sub>3</sub>Br, 99.5%), lead bromide (PbBr<sub>2</sub>, 99.9%), N, N-dimethylformamide (DMF, 99%), and dimethylsulfoxide (DMSO, 99%) were used as received. CH<sub>3</sub>NH<sub>3</sub>Br and PbBr<sub>2</sub> with a molar ratio of 1:1 were dissolved in DMSO-DMF (7:3 by volume) to form a solution, and MAPbBr<sub>3</sub> was crystallized at the temperature from 60 °C to 100 °C with a heating rate of 0.2 - 0.5 °C/h.

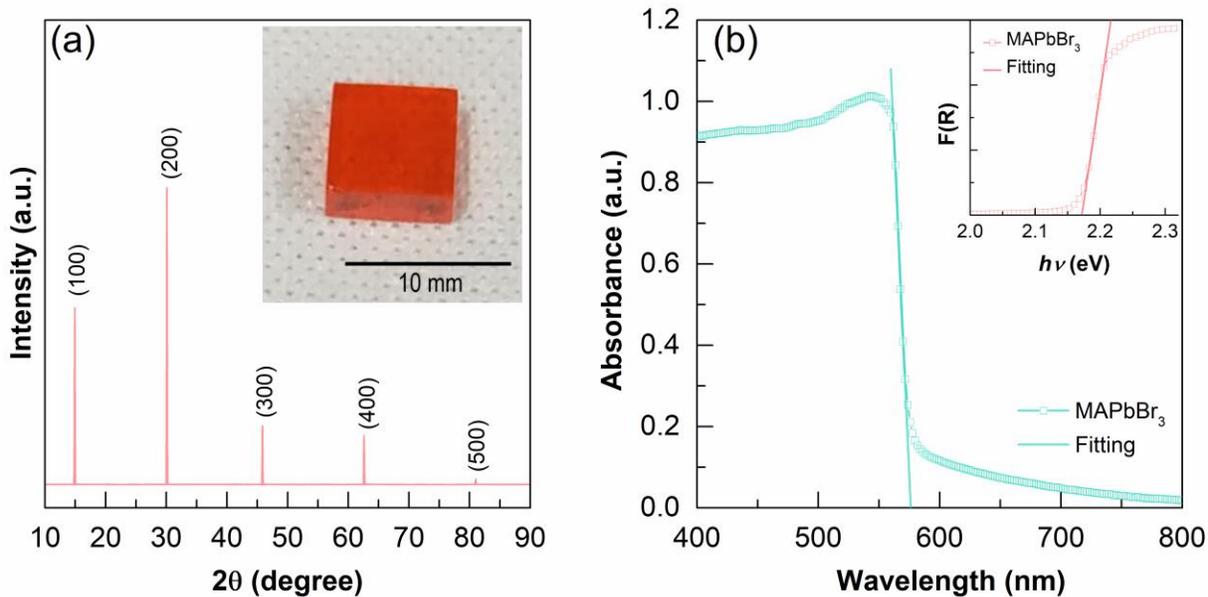
An X-ray diffractometer (XRD, PANalytical X'pert) was used for phase identification at room temperature. The absorption spectra were measured in the wavelength range of 400–800 nm using an UV spectrometer (SpectraPro-275, Acton Research Corporation). Au (100 nm) was used as the electrode for electrical characteristics with different sizes. The current-voltage (*I-V*)

measurements were carried out using a semiconductor analyzer (FS380, Platform Design Automation, Inc.) assisted with a probe station (Lakeshore TTPX). To avoid the influence of the environmental gases and moisture [21], the samples were kept in a vacuum chamber for electrical characteristics with a pressure of  $10^{-4}$  mbar.

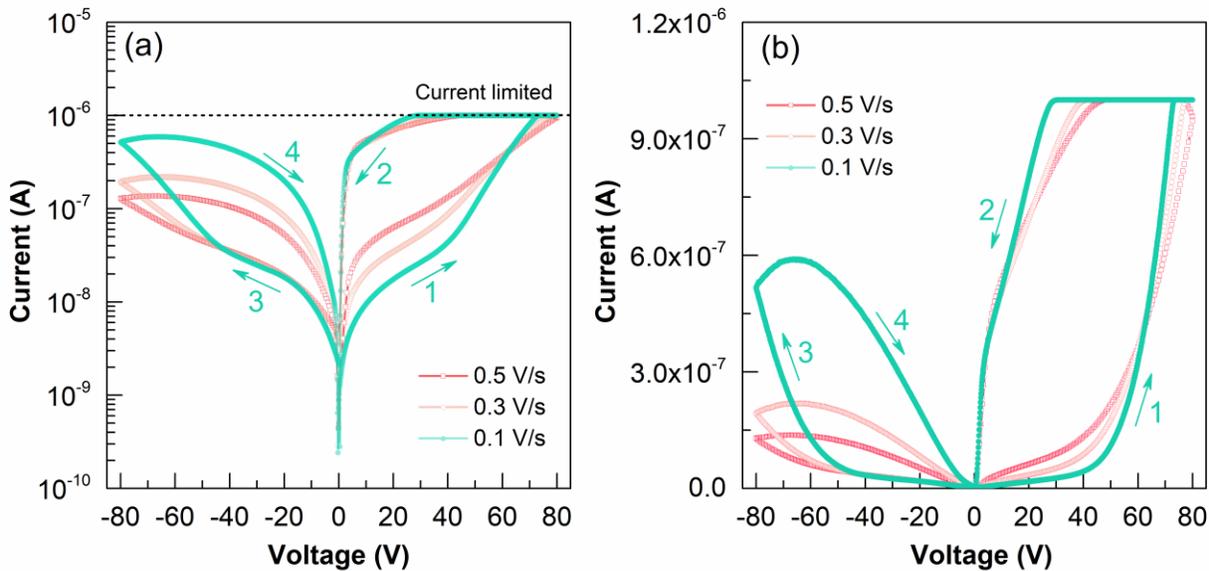
### 3. Results and Discussion

The inset of Figure 1a shows an optical image of an MAPbBr<sub>3</sub> single crystal with dimensions of  $\sim 6 \times 7 \times 2$  mm<sup>3</sup>. Figure 1a presents the typical XRD  $2\theta$  scan data of the MAPbBr<sub>3</sub> single crystal at room temperature. The crystal adopts a pure cubic phase with good crystallization and high quality. The absorbance of MAPbBr<sub>3</sub> (Figure 1b) shows a clear band edge cutoff with no excitonic signature, which suggests a minimal number of in-gap defect states [22]. The optical band gap of MAPbBr<sub>3</sub> could be determined to be 2.17 eV by extrapolating the linear region of the absorption edge to the energy-axis intercept, which is similar to the other reports [18].

Figure 2 shows the dark current of the Au/MAPbBr<sub>3</sub>/Au structure with different scanning rates and directions in two different scale. The arrows in the figure depict the bias scanning directions. The sequence of voltage sweep is  $0 \rightarrow +80 \text{ V} \rightarrow 0 \rightarrow -80 \text{ V} \rightarrow 0$ , where the voltage is termed as positive when the top electrode is positively biased. A clear switchable diode-like RS behavior is observed in Figure 2, which is quite different from the previous work on HOIP thin film devices [9-16]. In most of these films, only resistive switching but no diode-like behavior was mentioned. The diode direction can be switched during the sweep process in the dark. During the scan path 2, the device switches to the LRS. When the scan direction reverses in the path 3 the current abruptly decrease and the device switches to HRS. The on/off ratio of the dark current is  $\sim 54$  at a bias of +10 V.



**Figure 1.** (a) XRD and (b) absorption spectra of MAPbBr<sub>3</sub> single crystals. Inset: (a) image of one of the measured crystals grown from solution; (b) absorbance versus photon energy and the determined band gap  $E_g$ .

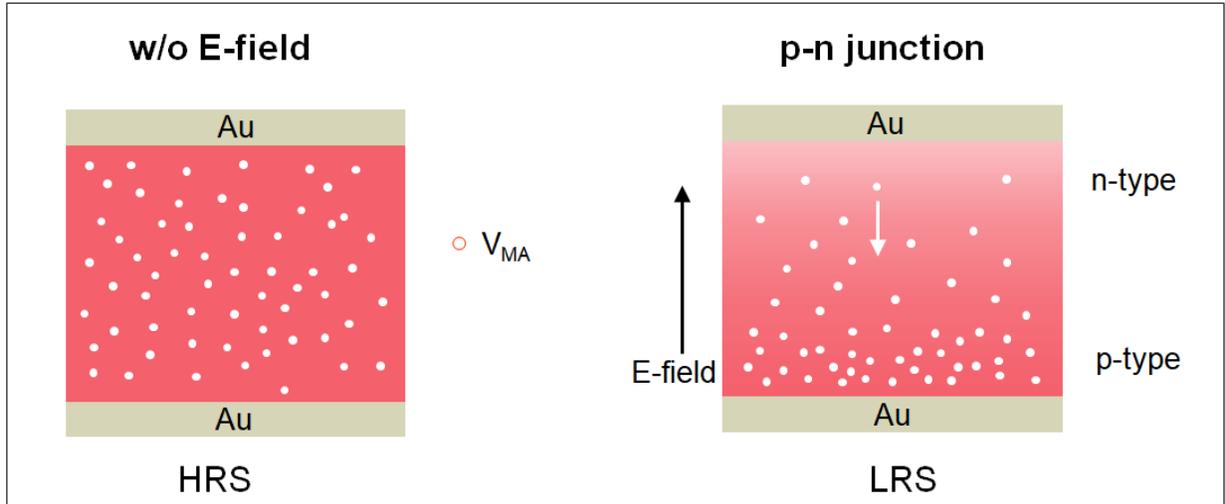


**Figure 2.** Current-voltage traces.  $I$ - $V$  curves of an Au/MAPbBr<sub>3</sub>/Au device in (a) log-scale and (b) normal-scale in the dark with different scanning rates. All the measurements were conducted at room temperature in a vacuum chamber.

We now focus on the origin of the switchable diode-like resistive switching. The switchable diode-like RS generally occurs in ferroelectric devices [23], where the barrier height at the ferro/electrode interface could be controlled by the switchable ferroelectric polarization. Actually, the switchable diode behavior has been observed in Au/MAPbI<sub>3</sub>/ITO thin film devices, which results in a giant switchable photovoltaic effect under illumination [24]. According to the proposed mechanism in refs. 28 and 29, since MAPbBr<sub>3</sub> is a non-ferroelectric, the switchable diode can be understood in terms of the defects electromigration process, as shown in Figure 3. It is known that many types of point defects have been reported in the HOIPs, such as V<sub>Pb</sub>, V<sub>Br</sub>, V<sub>MA</sub>, etc. Positively-charged Br vacancy (V<sub>Br</sub>) can lead to n-type doping, whereas negatively charged Pb and MA vacancies (V<sub>Pb</sub> and V<sub>MA</sub>) result in p-type doping [25]. Provided that the defects are mobile charges, they can move through the sample under electric fields to find a new thermodynamic equilibrium. For instance, V<sub>MA</sub> can move and pile up near the bottom surface for negative bias conditions. Thus, the top surface region acquires n-type carriers, whereas the region near the bottom electrode becomes p-type. Yuan *et al.* have reported a direct observation of MA<sup>+</sup> ion and V<sub>MA</sub> migration under a bias field [26]. The reversible p-n structures induced by ion drift has also been confirmed in HOIPs [24].

Noting that the rectifying p-n junction created through the asymmetric distribution of charged defects is also believed to induce of a RS behavior directly [27]. The low resistance state (LRS) can be attributed to the creation of p-n junction. By applying an opposite electric field, the inhomogeneous donor/or acceptor distribution can be reversed to an evenly distributed insulating state (HRS). For a full sweep cycle (Figure 2a), scan path 1 (HRS) and 4 (LRS) are quite sensitive to the scanning rate, which just reveals the defects electromigration process. Path 1 displays the forming process of the p-n structure from an evenly defect distribution. After the

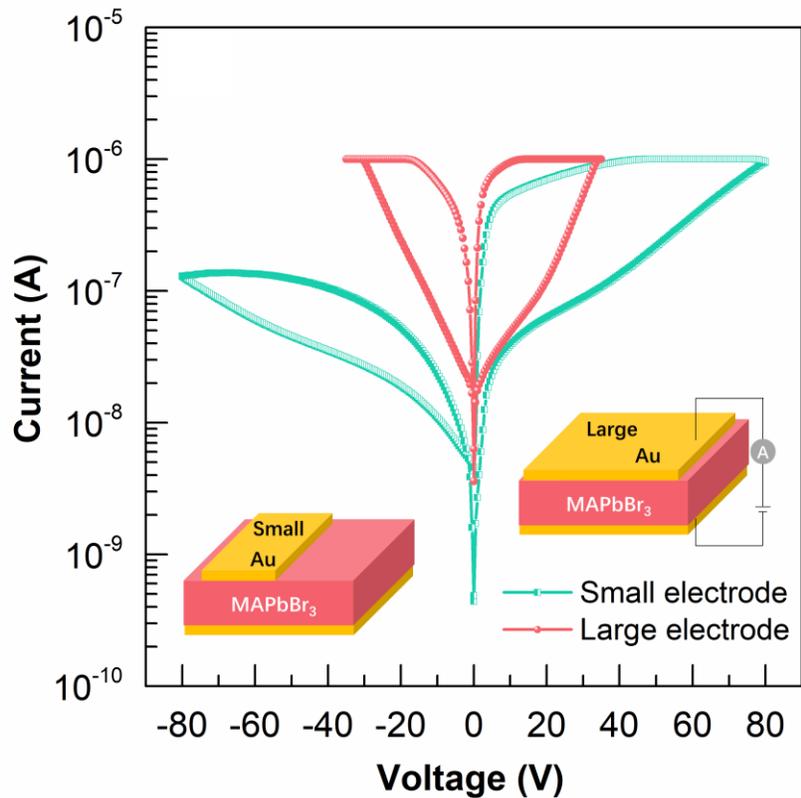
creation of inner p-n junction, the Au/MAPbBr<sub>3</sub>/Au structure shows a low resistance state and its current is then not sensitive to scan speed (path 2).



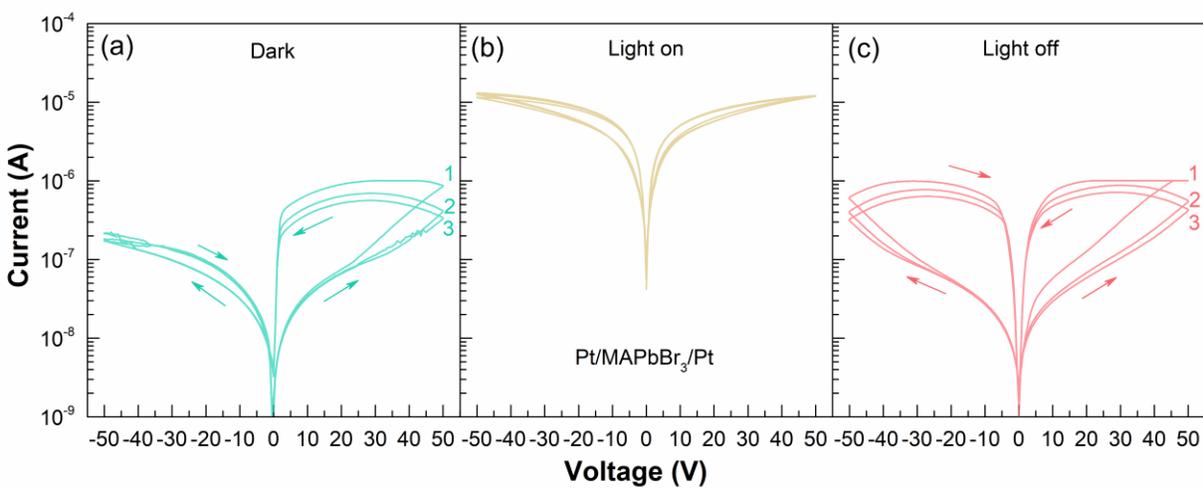
**Figure 3.** Schematic diagram describing the rectifying behavior of electronic states in MAPbBr<sub>3</sub> single crystal. Negatively charged  $V_{MA}$  is focused here, and the inclusion of other defects does not alter the conclusion. The MA vacancies respond to electric field. In a negative field (bottom to top), MA vacancies are moving downward and accumulating near the bottom surface and are frozen when the field is turned off. As a result, a state with p-type carriers is realized; the opposite region becomes a n-type conductor.

Notwithstanding, the p-n junction scenario cannot solely explain the asymmetrical and electrode-area dependent  $I$ - $V$  results. Figure 4 displays the RS behavior of an Au/MAPbBr<sub>3</sub>/Au device with different size of up-electrode. It can be seen that both the HRS and LRS are electrode area-dependent, which could be regarded as a fingerprint of an interface-type resistive switching [23]. Furthermore, as shown in Figure 2, the two hysteresis loops in one sweep cycle (path 1 to path 4) obtained under forward and reverse biases are asymmetrical, which further implies the interface-type RS nature. These asymmetrical loops can be observed more

conspicuously in a Pt/MAPbBr<sub>3</sub>/Pt device (see Figure 5a). These results imply that the interface conduction also plays an important role in the switchable diode-like resistive switching.



**Figure 4.** *I-V* curves of an Au/MAPbBr<sub>3</sub>/Au device in the dark with different up-electrode area.



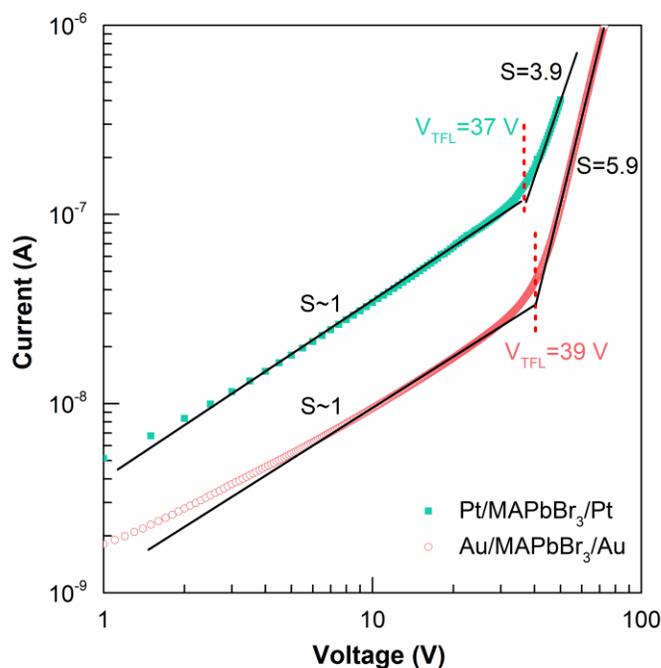
**Figure 5.** *I-V* curves of a Pt/MAPbBr<sub>3</sub>/Pt device (a) in the dark, (b) under light illumination (1500 LUX) and (c) subsequent light off.

The interface-type RS phenomenon has been explained by various conduction mechanisms such as trap-controlled SCLC, Schottky emission, and hopping conduction [9,28]. It was previously reported that Au and Pt form an Ohmic contact with HOIPs [22,29]. Then the interfacial Schottky barrier [10,16] scenario should be excluded in our case. To further understand the conduction mechanism, we replotted the  $I$ - $V$  curves of HRS in a log-log scale as shown in Figure 6. Two regions were evident in the HRS curves. At low voltages, the  $I$ - $V$  response was ohmic ( $I \propto V$ ). At high fields, the current exhibited a rapid non-linear rise and signaled the transition onto the trap-filled limit (TFL) regime, in which all the available trap states were filled by the injected carriers [30]. The  $I$ - $V$  response of the trap-filled SCLC is marked by a steep increase in current ( $I \propto V^{n>3}$ ). The onset voltage  $V_{TFL}$  ( $V_{TFL} = 37$  V for Pt electrodes and 39 V for Au electrodes) is typically linearly proportional to the density of trap states as follows [31],

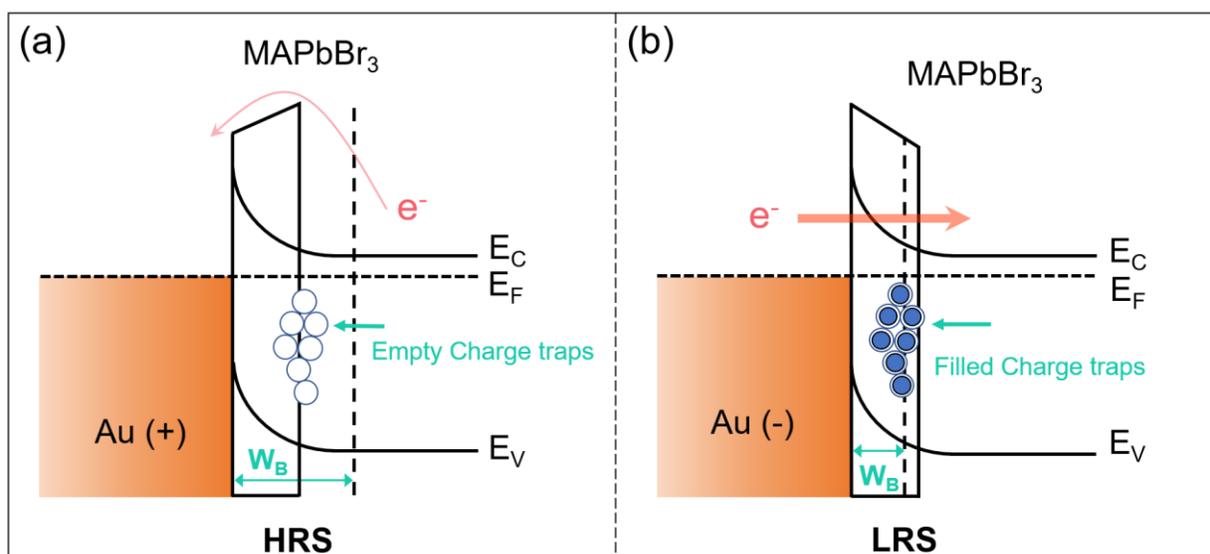
$$V_{TFL} = 8en_t d^2 / 9\epsilon\epsilon_0 \quad (1)$$

Where  $n_t$  is the density of trap states,  $d$  is the thickness of the sample,  $\epsilon$  is the dielectric constant (here we use 25 for MAPbBr<sub>3</sub>) [32], and  $\epsilon_0$  is the vacuum permittivity. Correspondingly, it could be found that our MAPbBr<sub>3</sub> single crystals have a trap density of  $\sim 1.5 \times 10^{12} \text{ cm}^{-3}$ .

According to the above area-dependent results, we propose partially an interface RS mechanism based on charge trapping/detrapping at the perovskite/Au (or Pt) interfacial trapping centers. As illustrated in Figure 7, the resistance is determined by the filling state of the interface charge traps. Charge traps could be introduced during the crystal growth procedure and can thus be controlled for the RS properties. At the initial state, the traps are empty and leads to the HRS by increasing the tunneling barrier width. If all the traps are filled, the injected carriers can move freely into the perovskite crystals and then lead to a low resistance state.

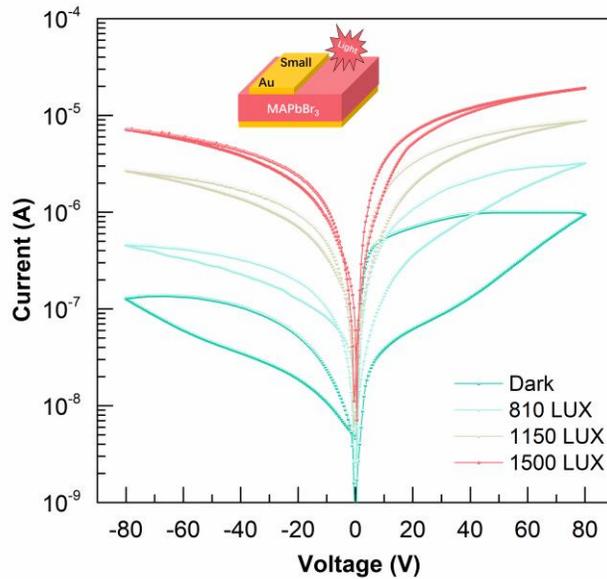


**Figure 6.** The high resistance state of MAPbBr<sub>3</sub> single crystals with Au and Pt electrodes in a log-log scale. Characteristic *I-V* traces showing two different regimes. A linear ohmic regime ( $I \propto V$ ) is followed by the trap-filled regime ( $I \propto V^{n>3}$ ).



**Figure 7.** (a, b) the trap/detrapping and tunneling of electrons. When a positive voltage is applied to the electrode, this results in charge trapping, narrowing of the interfacial barrier width and induces an HRS-to-LRS switching.

Interestingly, the trap states at the perovskite/Pt (or Au) interface can be modulated by light absorption. Figure 5a, b, and c show the  $I$ - $V$  curves of a Pt/MAPbBr<sub>3</sub>/Pt device in the dark, under light illumination and subsequent light off, respectively. For the initial measurement in the dark, the switching loop under reverse bias is severely restricted, which means the traps were not fully filled in this sweep process. After light illumination, some of the interface traps are “activated” and then induce an obviously resistance switching behavior (Figure 5c). Figure 8 illustrates the evolution of RS behavior of Au/MAPbBr<sub>3</sub>/Au device under light illumination with different intensity. Under the light illumination, both the HRS and LRS currents increase due to the existence of photocurrent. The resistive switching loops became narrower than the dark case, and wore off eventually. The results of both area-dependent and light-responsive experiments exclude the possible of metallic filaments mechanism for the observed RS behavior.



**Figure 8.**  $I$ - $V$  curves of an Au/MAPbBr<sub>3</sub>/Au device under light illumination with selected power density.

On the other hand, the interface charge trapping/detrapping cannot either solely explain the switchable diode-like RS behavior. Because the interface charge trapping state cannot change obviously when the voltage is just reversed, which cannot induce a diode-like behavior. It is worth noting that the trap-controlled SCLC could be used to describe not only the conduction of an interface charge trapping/detrapping process, but also the defects electromigration process [24,27]. The aforementioned switchable diode-like RS behavior can be then explained by the formation of an electric-field-driven p-n junction accompanied by a trap-controlled SCLC at the electrode-perovskite interface. The resistance state (HRS and LRS) depends on the synergy between the inner p-n junction and the interface.

#### **4. Conclusions**

In summary, a switchable diode-like RS behavior was observed in MAPbBr<sub>3</sub> single crystals using Au (or Pt) symmetry electrodes. The dependence of the surface barrier on the interface trap filling level plays a vital role on the resistance switching. The emptied traps cause a wide tunneling barrier height and correspondingly cause a high resistance state. When the injected carriers fill the traps in the surface space charge region, the barrier was narrowed and the device switches to the LRS. During the interface trap filling or emptying processes, the charged defects also migrate under applying a bias and lead to a reversible p-n structure in the crystal.

#### **Declaration of Competing Interest**

The authors declare that they have no known competing financial interests or personal relationships that could have appeared to influence the work reported in this paper.

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